



HIGH PERFORMANCE DIGITAL SYSTEMS AND APPLICATIONS CONFERENCE

From silicon to systems

nmi
Electronic Systems
Design

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UNIVERSITY OF WARWICK

High performance digital systems are increasingly required in modern life, being used to implement a diverse range of applications. These applications impact many aspects of our lives and include Future Autonomous Vehicles, Artificial Intelligence, Machine Learning, High Performance Computing, Cloud Computing / Data Centres along with Virtual and Augmented Reality.

Depending upon the deployment and use case these applications may need to implement conflicting requirements. One common conflicting requirement for edge-based applications is the need for increased processing capability, along with a reduced power budget as the application is power constrained.

Addressing these requirements and constraints, demands a systematic approach at the architectural level where cloud or edge compute may be considerations. To considering the potential implementation technologies which can be used for the solution CPU, GPU, FPGA or SoC.

Regardless of the solution chosen, time to market is key and developers need to leverage industry standard development environments, frameworks, libraries and Intellectual Property often supplied by a third party to achieve development timescales.

This results in a complex development environment, which includes hardware and software co-development and regulatory compliance in some applications resulting in a complex verification and validation strategy.

This event will be of interest to System Architects, Electronic, Software and Chip Engineers, and Engineering Managers working in this area and in the variety of application areas that have the requirement for increased system performance.



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