



## Conference Agenda

Day 1

12<sup>th</sup> May 2009

Theme: Understanding Designing for Variability ( DfV )

8:30: Registration and Refreshments

### Welcome & Introduction

9:30am Paul Jarvie, National Microelectronics Institute, "Welcome to ICCV2009"

9:50am Conference Opening Address

### SESSION 1: The IDM & Fabless perspective

10:10am Key Note: Dr Kelin Kuhn ,Intel, "Variation in 45nm and implications for 32nm and beyond"



10:55am Coffee Break

11:15am Krisztian Flautner, ARM , "Life on the Treadmill"

11:45am Professor Asen Asenov, University of Glasgow , "Statistical variability and reliability in the next generation CMOS technologies and the ITRS response"

12:15pm Buffet Lunch & Networking

### SESSION 2: Embracing DfV in the Foundry

1:45pm Key Note: Dr Sani Nassif, IBM, "Variability beyond 45nm: design impact and countermeasures"



2:30pm Davide Pandini , ST Microelectronics, "Statistical Static Timing Analysis: A New Approach to Deal with Increased Process Variability in Advanced Nanometer Technologies"

3:00pm Coffee Break

3:20pm Nam Sung Kim, Chartered Semiconductors , "SRAM Variability: Foundry Perspectives"

3:50pm PANEL: Are we ready for DfV at sub 32nm ?

Chair : Professor Asen Asenov

4:30pm Conference Close ( DAY 1 )

7:00~10:00pm Conference Dinner : Savoy Place, London, UK , WC2R 0BL

Theme: *Facing up to Variability*

**8:30am Registration and Refreshments**

**Welcome & Introduction DAY2**

**9:30am Paul Jarvie**, National Microelectronics Institute, "Welcome to ICCV2009"

**SESSION 1: The Era of Variability: Are EDA Tools ready ?**

**9:40am Key Note: Jean-Marie Brunet**, Mentor Graphics,



"Managing Variability in next generation design methodologies and tools "

**10:25am Andrew Appleby**, NXP Semiconductors, "Realising a 45nm System on Chip for digital TV in the age of variability"

**10:55am Coffee Break**

**11:15am Dr. Ahsan Bootehsaz** , Synopsys, "Designing with variability: An EDA perspective"

**11:45am Miguel Miranda**, IMEC, "Design for Variability efforts in Europe - REALITY project "

**12:15pm Buffet Lunch & Networking**

**SESSION 2: Delivering Product Performance in the face of Variability**

**1:15pm Sharad Saxena** , PDF Solutions, "Variability Characterization Infrastructure for Nanometer-Scale Technologies"

**1:45pm Philippe Hurat**, Cadence, "Analyzing, preventing and correcting the impact of manufacturing variation in advanced node IC design, including physical and electrical effects induced by Litho, CMP and Stress"

**2:15pm Coffee Break**

**2:35pm Jeff Watt**, Altera, "Variability-Aware Product Development in a Fabless Environment"

**3:05pm Patrick Drennan**, Solido Design Automation, "Design for Variability in Analog / Mixed-Signal Circuits"

**3:35pm PANEL: "If Variability is real: Are we doing enough about it?"**

Chair : **Ian Phillips** , ARM

**4:15 Conference Close & Wrap up**