

National
Microelectronics
Institute



ICCV 2009: 2nd International Conference
on CMOS Variability

“Living with Variability”

**Mentor
Graphics®**



SYNOPSYS®



London, 12/13th May 2009



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The screenshot displays the NMinet website, a social networking platform for the microelectronics industry. The top navigation bar includes links for Home, About Us, Design Innovation, Manufacturing, Industry Activities, Publications & Downloads, and Events. The main header area features the NMI logo and a welcome message for Paul C Jarvie. Below this, a 'MEMBER SPOTLIGHT' section highlights a member. The central content area is divided into several sections: 'FEATURES' with a news item about the NMI Conference, 'Photos' showing a group of people, 'Members' with a grid of user avatars, and a 'Forum' section. A right-hand sidebar provides a user profile for Paul C Jarvie, including options to sign out, view inbox, contacts, and settings, as well as a 'Quick Add...' dropdown. At the bottom, there is an 'EETimes RSS Feed' and a status bar indicating 'NMinet | 1 Online'.

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The National Microelectronics Institute

NMI is the premier trade association for the microelectronics industry in the UK and Ireland. Our mission is to support the semiconductor community and act as a catalyst for development.

Find out more about the NMI »

MEMBER SPOTLIGHT

NMI is hosting Europe's only conference on CMOS variability, bringing together world-leading experts from Intel, ARM, Mentor Graphics and more. Come and explore the future of the industry.

Happy Scaling is Over! NMI Conference faces the challenge

NMI is hosting Europe's only conference on CMOS variability, bringing together world-leading experts from Intel, ARM, Mentor Graphics and more. Come and explore the future of the industry.

Welcome to NMinet, Paul C Jarvie!

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2nd International Conference on CMOS Variability- Agenda Day 2



8:30am Registration and Refreshments

Welcome & Introduction DAY2

9:30am **Paul Jarvie**, National Microelectronics Institute, "Welcome to ICCV2009"

SESSION 1: The Era of Variability: Are EDA Tools ready ?

9:40am **Key Note: Jean-Marie Brunet**, Mentor Graphics,
"Managing Variability in next generation design methodologies and tools "

10:25am **Andrew Appleby**, NXP Semiconductors, "Realising a 45nm System on Chip for digital TV in the age of variability"

10:55am **Coffee Break**

11:15am **Dr. Ahsan Bootehsaz** , Synopsys, "Designing with variability: An EDA perspective"

11:45am **Miguel Miranda**, IMEC, "Design for Variability efforts in Europe - REALITY project "

12:15pm Buffet Lunch & Networking

SESSION 2: Delivering Product Performance in the face of Variability

1:15pm **Sharad Saxena** , PDF Solutions, "Variability Characterization Infrastructure for Nanometer-Scale Technologies"

1:45pm **Philippe Hurat**, Cadence, "Analyzing, preventing and correcting the impact of manufacturing variation in advanced node IC design, including physical and electrical effects induced by Litho, CMP and Stress"

2:15pm **Coffee Break**

2:35pm **Jeff Watt**, Altera, "Variability-Aware Product Development in a Fabless Environment"

3:05pm **Patrick Drennan**, Solido Design Automation, "Design for Variability in Analog / Mixed-Signal Circuits"

3:35pm **PANEL: "If Variability is real: Are we doing enough about it?"**

Chair: Ian Phillips , ARM

4:15 **Conference Close & Wrap up**

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